

**UNIVERSITY OF ASIA PACIFIC**

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| - Transient Analysis of NAND and NOR Gates- LAB1 Pending task (Inverter Symbol) |

**Course Code :** CSE 458

**Course Title :** Design & Testing of VLSI Lab

**Submitted by: Submitted To: Name :** Sheikh Nafez Sadnan Syeda Fatima Fayruj **Reg. No. :** 20101106 Lecturer

**Roll No. :** 106 Department of EEE

**Section :** B (2) University of Asia Pacific